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1 [A graphical criterion for the identification of causal effects in linear models](#)

Carlos Brito, Judea Pearl

July 2002 Eighteenth national conference on Artificial intelligence

Publisher: American Association for Artificial Intelligence**Additional Information:** [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall):

This paper concerns the assessment of direct causal effects from a combination of: (i) and (ii) qualitative domain knowledge. Domain knowledge is encoded in the form of a (DAG), in which all interactions ...

2 [Post-compaction register assignment in a retargetable compiler](#)

Philip Sweeney, Steven Beaty

November 1990 **MICRO 23**: Proceedings of the 23rd annual workshop and symposium on microarchitecture**Publisher:** IEEE Computer Society Press

Full text available: Pdf (998.12 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)**Bibliometrics:** Downloads (6 Weeks): 1, Downloads (12 Months): 7, Downloads (Overall): 114

We discuss graph-coloring register assignment in a retargetable compiler for Long-In. Of specific concern is when, during the compilation process, should register assignment conclude that, for best results, ...

3 [Tradeoff routing resource, runtime and quality in buffered routing](#)

Xiaoping Tang, Martin D. F. Wong

January 2004 **ASP-DAC '04**: Proceedings of the 2004 Asia and South Pacific Design Auto**Publisher:** IEEE Press

Full text available: Pdf (114.25 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)**Bibliometrics:** Downloads (6 Weeks): 0, Downloads (12 Months): 4, Downloads (Overall): 38,

With the wide use of hard macros and IP blocks in design, buffered routing (simultaneous insertion) becomes unavoidable. Routing resource allocation and distribution are serious routing of deep submicron design. ...

4 [The management of applications for reconfigurable computing using an operating](#)

Grant Wigley, David Kearney

January 2002 **CRPIT '02**: Proceedings of the seventh Asia-Pacific conference on Comput**Publisher:** Australian Computer Society, Inc.

Full text available: Pdf (883.77 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)**Bibliometrics:** Downloads (6 Weeks): 0, Downloads (12 Months): 15, Downloads (Overall): 60

As the number of system gates available on reconfigurable platforms increase beyond

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management of these resources and their sharing among many applications and user concern. This paper identifies the ...

Also published in:

January 2002 **Australian Computer Science Communications** Volume 24 Issue 3

5 Visualizing software for telecommunication services

 Emden R. Gansner, John M. Mccenigo, Stephen C. North
June 2003 **SoftVis '03: Proceedings of the 2003 ACM symposium on Software visualization**

Publisher: ACM  [Request Permissions](#)

Full text available:  Pdf (390.04 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 36, Downloads (Overall): 40

An active research area in telecommunications concerns how to specify and control the architecture such as call waiting or instant messaging, into existing software. One approach is to use a distributed architecture such as Distributed ...

Keywords: algorithm animation, graph layout

6 Checking signal transition graph implementability by symbolic BDD traversal

A. Kondratyev, J. Cortadella, M. Kishinevsky, E. Pastor, O. Roig, A. Yakovlev
March 1995 **EDTC '95: Proceedings of the 1995 European conference on Design and Test**

Publisher: IEEE Computer Society

Full text available:  Publisher Site ,  Pdf (861.20 KB) **Additional Information:** [full citation](#), [abstract](#), [references](#), [citations](#)

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 16, Downloads (Overall): 70

This paper defines conditions for a Signal Transition Graph to be implemented by an hierarchy of the implementability classes is presented. Our main concern is the implementation specification under the restricted input-output ...

Keywords: STG implementability, asynchronous circuit design, asynchronous circuit implementability classes, logic CAD, restricted input-output interface, sequential circuit signal transition graph, symbolic BDD traversal, symbolic approach

7 Problem solving performance and display preference for information displays design

 Mary J. LaLomia, Michael D. Coover, Eduardo Salas

October 1988 **SI GCHI Bulletin** , Volume 20 Issue 2

Publisher: ACM

Full text available:  Pdf (545.62 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 9, Downloads (Overall): 120


As the amount of information accessible via computer systems increases, so do problems presenting information that can be assimilated easily and accurately. Many researches presentation of information to allow ...

8 The role of software engineering in the microcomputer revolution: An overview

W. M. McKeeman

September 1979 **ICSE '79: Proceedings of the 4th international conference on Software Engineering**

Publisher: IEEE Press

Full text available:  Pdf (63.37 KB)


Additional Information: [full citation](#), [abstract](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 12, Downloads (Overall): 60

The title of this paper is presumptuous. It presumes that software engineering exists and a reasonably common understanding of it. It presumes that there is a microcomputer revolution.

it is. And it presumes that software ...


9 Topologies' - computational messaging for multicomputers

 K. Schwan, W. Bo

January 1988 **C'P: Proceedings of the third conference on Hypercube concurrent applications: Architecture, software, computer systems, and gener.**

Volume 1

Publisher: ACM  Request Permissions

Full text available:  Pdf (1.57 MB)

Additional Information: full citation, abstract, references, c

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 9, Downloads (Overall): 12f


The programming of a large-scale multicomputer that exhibits an interconnection stru applications programmer (e.g. a hypercube or mesh) requires the explicit constructio communication structures for connecting the ...

10 Developing a SARA building block - the 8080

William T. Overman, Gerald Estrin

February 1977 **Proceedings of the Symposium on Design Automation and Microprocessor**

Publisher: IEEE Press


Full text available:  Pdf (628.73 KB)

Additional Information: full citation, abstract, references, c

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 2, Downloads (Overall): 78,


This paper concerns itself with issues in development of building blocks for use in str computer systems and bases its discussion on experiences with behavioral modelling The SARA (System ...

11 Interprocedural slicing using dependence graphs

 Susan Horwitz, Thomas Reps, David Binkley

April 2004 **SIGPLAN Notices**, Volume 39 Issue 4

Publisher: ACM

Full text available:  Pdf (1.95 MB)

Additional Information: full citation, abstract, references

Bibliometrics: Downloads (6 Weeks): 14, Downloads (12 Months): 106, Downloads (Overall):


A slice of a program with respect to a program point p and variable x consists of all s that might affect the value of x at point p . This paper concerns the problem of interpi generating ...

12 Integrating semantics and flexible syntax by exploiting isomorphism between relations

Morena Daniele, Franco Ferrara, Roberto Gemello, Claudio Rullent

April 1987 **Proceedings of the third conference on European chapter of the Association Linguistics**

Publisher: Association for Computational Linguistics

Full text available:  Publisher Site ,  Pdf (446.61 KB) **Additional Information:** full citation, abstract, refer

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 2, Downloads (Overall): 88,


This work concerns integration between syntax and semantics. Syntactic and semant bodies of knowledges. Integration is obtained by exploiting the isomorphism between (among immediate constituents) and ...

13 Factors in interprocess communication protocol efficiency for computer networks

 Carl A. Sunshine

June 1976 **AFIPS '76: Proceedings of the June 7-10, 1976, national computer confere**

Publisher: ACM  Request Permissions

Full text available:  Pdf (734.51 KB)

Additional Information: full citation, abstract, references

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 36, Downloads (Overall): 4

This paper considers the efficiency of interprocess communication protocols for distribut environments such as computer networks. Previous research has emphasized system levels, within the communication medium itself, ...

14 [A general identification condition for causal effects](#)

Jin Tian, Judea Pearl

July 2002 Eighteenth national conference on Artificial intelligence


Publisher: American Association for Artificial Intelligence

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall):

This paper concerns the assessment of the effects of actions or policy interventions fr nonexperimental data, and (ii) substantive assumptions. The assumptions are encode acyclic graph, also called "causal ...

15 [Solving shape-analysis problems in languages with destructive updating](#)

 Mooly Sagiv, Thomas Reps, Reinhard Wilhelm

January 1998 **Transactions on Programming Languages and Systems (TOPLAS)** ,

Publisher: ACM  [Request Permissions](#)

Full text available:  Pdf (703.82 KB)


Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 47, Downloads (Overall): 50

This article concerns the static analysis of programs that perform destructive updatin We give an algorithm that uses finite shape graphs to approximate conservatively the heap-allocated ...


Keywords: abstract interpretation, alias analysis, dataflow analysis, destructive up shape analysis, shape graphs, static analysis

16 [FPV: fast protein visualization using Java 3D™](#)

 Toiga Can, Yujun Wang, Yuan-Fang Wang, Jianwen Su

March 2003 **SAC '03: Proceedings of the 2003 ACM symposium on Applied computing**

Publisher: ACM  [Request Permissions](#)

Full text available:  Pdf (2.34 MB)


Additional Information: [full citation](#), [abstract](#), [references](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 13, Downloads (Overall): 20

We have developed a protein visualization system based on Java 3D™. Java 3D provi different systems and enables applications to be run remotely through web browsers. visualization has some performance ...

Keywords: Java 3D, performance analysis, protein visualization, scene graph optimi

17 [A student-designed Undergraduate program](#)

 Michael A. Bauer

January 1970 **SIGCSE '70: Proceedings of the first SIGCSE technical symposium on Edu**

Publisher: ACM  [Request Permissions](#)


Additional Information: [full citation](#), [abstract](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall):


In recent years there has been a growing interest and concern among professionals Science about undergraduate and graduate programs. A student could possibly offer

area, being immediately involved in ...

18 Multimodal response generation in GIS

 Levent Bojelli

October 2004 **ICMI '04**: Proceedings of the 6th international conference on Multimodal in
Publisher: ACM


Full text available:  Pdf (161.26 KB)

Additional Information: [full citation](#), [abstract](#), [references](#)

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 41, Downloads (Overall): 25


Advances in computer hardware and software technologies have enabled sophisticated techniques as well as new interaction opportunities to be introduced in the development of Information Systems) applications. ...

19 A fast, memory-efficient register allocation framework for embedded systems

 Sethyanarayanan Thammanur, Santosh Pande

November 2004 **Transactions on Programming Languages and Systems (TOPLAS)**

Publisher: ACM 

Full text available:  Pdf (1.01 MB)


Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 38, Downloads (Overall): 75

In this work, we describe a "just-in-time," ϵ -usage density-based register allocator for embedded systems with a limited general-purpose register set wherein speed, code size requirements are of equal concern. The main ...


Keywords: Code generation, compiler optimizations, compilers, dynamic compilation, register allocation

20 Piecing together complexity

 Karen A. Frenkel

February 1986 **Communications of the ACM**, Volume 29 Issue 2

Publisher: ACM 

Full text available:  Pdf (312.35 KB)




Additional Information: [full citation](#), [abstract](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 24, Downloads (Overall): 15

To illustrate the "remarkable extent to which complexity theory operates by means of theory," Richard Karp created this conceptual map or jigsaw puzzle. To lay out the puzzle "graph planarity algorithm." ...

Result

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